WHAT IS CLAIMED IS:

1. A method, comprising:

accessing data representing an interconnect model, wherein the interconnect model includes a driving point node, and wherein the interconnect model is not a lumped capacitance model;

calculating an effective capacitance of the interconnect model to be inversely proportional to a voltage at the driving point node of the interconnect model; and

storing a value representing the effective capacitance.

2. The method of claim 1, wherein

said calculating calculates the effective capacitance to be directly proportional to a sum of one or more products, wherein each of the one or more products equals a product of a respective one of one or more capacitances included in the interconnect model and a voltage across the respective one of the one or more capacitances.

3. The method of claim 1, wherein

said accessing further comprises accessing data representing a driver model; and

said calculating comprises calculating the effective capacitance as a function of a resistance included in the driver model.

4. The method of claim 1, wherein

said calculating is performed without using numerical techniques.

5. The method of claim 1, wherein

the interconnect model is a pi model.

6. The method of claim 5, further comprising:

calculating a plurality of time constants from a plurality of capacitances and a resistance included in the pi model and from a resistance included in a driver model of a driver coupled to an interconnect modeled by the interconnect model; and

using the plurality of time constants to perform said calculating the effective capacitance.

7. The method of claim 1, wherein:
the interconnect model includes one or more inductances.

8. The method of claim 1, wherein:
said calculating the value of the effective capacitance is performed according to a closed form algorithm.

9. The method of claim 1, wherein
said storing comprises storing the effective capacitance value in a lookup table.

10. The method of claim 9, further comprising:
repeating said calculating and said storing for each of a plurality of different values of the one or more capacitances in the interconnect model.

11. A method, comprising:
scaling a first capacitance included in an interconnect model by a ratio of a voltage across the first capacitance to a driving point voltage;
scaling a second capacitance included in an interconnect model by a ratio of a voltage across the second capacitance to the driving point voltage;
summing the first scaled capacitance and the second scaled capacitance to produce an effective capacitance value; and
storing the effective capacitance value;
wherein the driving point voltage is a voltage at an input to the interconnect model.

12. The method of claim 11, wherein
the interconnect model is a pi model.

13. The method of claim 12, further comprising:
calculating a plurality of time constants from the first capacitance and the second capacitance; and
using the plurality of time constants to perform said scaling the first capacitance, said scaling the second capacitance, and said summing.

14. The method of claim 11, wherein:
the interconnect model includes one or more inductances.

15. The method of claim 11, wherein:
said scaling the first capacitance, said scaling the second capacitance, and said summing are performed according to a closed form algorithm.

16. The method of claim 11, wherein
said storing comprises storing the effective capacitance value in a lookup table included in a library.

17. A method, comprising:
multiplying each one of a plurality of capacitances included in an interconnect model by a respective voltage across that one of the plurality of capacitances to generate a respective one of a first plurality of products;
dividing each one of the plurality of products by a voltage at a driving point node of the interconnect model to produce a respective one of a second plurality of products, wherein the interconnect model is not a lumped-capacitance model; and
summing the second plurality of products to produce an effective capacitance value; and
storing the effective capacitance value.

18. A method, comprising:
multiplying each one of a plurality of capacitances included in an interconnect model by a respective voltage across that one of the plurality of capacitances to generate a respective one of a first plurality of products;
summing the first plurality of products to produce a first value; and
dividing the first value by a voltage at a driving point node of the interconnect model to produce an effective capacitance value, wherein the interconnect model is not a lumped-capacitance model;
storing the effective capacitance value.

19. A system, comprising a processor and a memory storing program instructions executable by the processor to:

access data representing an interconnect model, wherein the interconnect model includes a driving point node, and wherein the interconnect model is not a lumped capacitance model;

calculate an effective capacitance of the interconnect model to be inversely proportional to a voltage at the driving point node of the interconnect model; and

store a value representing the effective capacitance.

20. The system of claim 19, wherein the program instructions are executable by the processor to:

calculate the effective capacitance to be directly proportional to a sum of one or more products, wherein each of the one or more products equals a product of a respective one of one or more capacitances included in the interconnect model and a voltage across the respective one of the one or more capacitances.

21. The system of claim 19, wherein the program instructions are executable by the processor to:

access data representing a driver model; and

calculate the effective capacitance as a function of a resistance included in the driver model.

22. The system of claim 19, wherein the program instructions are executable by the processor to:

calculate the effective capacitance without using numerical techniques.

23. The system of claim 19, wherein

the interconnect model is a pi model.

24. The system of claim 19, wherein:

the interconnect model includes one or more inductances.

25. The system of claim 19, wherein the program instructions are executable by the processor to:

calculate the effective capacitance according to a closed form algorithm.

26. The system of claim 19, wherein the program instructions are executable by the processor to:

store the value representing the effective capacitance in a lookup table.

27. A computer readable medium, comprising program instructions executable to:

access data representing an interconnect model, wherein the interconnect model includes a driving point node, and wherein the interconnect model is not a lumped capacitance model;

calculate an effective capacitance of the interconnect model to be inversely proportional to a voltage at the driving point node of the interconnect model; and

store a value representing the effective capacitance.

28. The computer readable medium of claim 27, wherein the program instructions are executable to:

calculate the effective capacitance to be directly proportional to a sum of one or more products, wherein each of the one or more products equals a product of a respective one of one or more capacitances included in the interconnect model and a voltage across the respective one of the one or more capacitances.

29. The computer readable medium of claim 27, wherein the program instructions are executable to:

access data representing a driver model; and

calculate the effective capacitance as a function of a resistance included in the driver model.

30. The computer readable medium of claim 27, wherein the program instructions are executable to:

calculate the effective capacitance without using numerical techniques.

31. The computer readable medium of claim 27, wherein

the interconnect model is a pi model.

32. The computer readable medium of claim 27, wherein:

the interconnect model includes one or more inductances.

33. The computer readable medium of claim 27, wherein the program instructions are executable to:

calculate the effective capacitance according to a closed form algorithm.

34. The computer readable medium of claim 27, wherein the program instructions are executable to:

store the value representing the effective capacitance in a lookup table.

35. A method, comprising:

accessing data representing an interconnect model, wherein the interconnect model includes a driving point node;

reducing the interconnect model to a lumped capacitance model consisting of an effective capacitance, wherein said reducing comprises:

calculating the effective capacitance by setting a voltage across the effective capacitance equal to a voltage at the driving point node of the interconnect model; and

storing a value representing the effective capacitance.

36. A computer readable medium, comprising program instructions executable to:

scale a first capacitance included in an interconnect model by a ratio of a voltage across the first capacitance to a driving point voltage;

scale a second capacitance included in an interconnect model by a ratio of a voltage across the second capacitance to the driving point voltage;

sum the first scaled capacitance and the second scaled capacitance to produce an effective capacitance value; and

store the effective capacitance value;

wherein the driving point voltage is a voltage at an input to the interconnect model.